

1 WHAT IS CLAIMED IS

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Sub A  
1. A display device comprising:  
a display unit which displays an image;  
memories which store information regarding  
control of said display unit;

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an operation circuit unit which controls  
said display unit to display the image based on the  
information stored in said memories;

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a data bus which connects said memories to  
an exterior of said display device, and supplies the  
information to said memories from the exterior of said  
display device; and

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an address bus which connects said memories  
to the exterior of said display device, and supplies  
address signals for selecting one of said memories.

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Sub B1  
2. The display device as claimed in claim 1,  
wherein said operation circuit unit includes:

a gate driver which drives gate lines of  
said display unit; and

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a data driver which drives data lines of  
said display unit, wherein at least one of said gate  
driver and said data driver operates based on the  
information stored in said memories.

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3. The display device as claimed in claim 2,  
wherein the at least one of said gate driver and said

1 data driver includes a shift-register which operates  
based on the information stored in said memories to  
control a scan direction of said display unit.

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4. The display device as claimed in claim 2,  
wherein the at least one of said gate driver and said  
10 data driver includes a decoder which operates based on  
the information stored in said memories to control a  
scan direction and a scan order of said display unit.

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5. The display device as claimed in claim 4,  
wherein the at least one of said gate driver and said  
data driver further includes an address counter which  
20 operates based on the information stored in said  
memories to supply an address to said decoder, said  
decoder decoding the address to control the scan  
direction and the scan order of said display unit.

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6. The display device as claimed in claim 2,  
wherein said memories store pattern data, said data  
30 driver operating in accordance with the pattern data  
stored in said memories to control said display unit  
to display an image corresponding to the pattern data.

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7. The display device as claimed in claim 6,

1 wherein said operation circuit unit further includes a  
data-synthesis circuit which combines the pattern data  
stored in said memories and display data supplied from  
the exterior of said display device to generate  
5 synthesized pattern data, said data driver operating  
in accordance with the synthesized pattern data to  
control said display unit to display an image  
corresponding to the synthesized pattern data.

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*Sub B2*  
15 8. The display device as claimed in claim 1,  
further comprising:

a display-information acquisition circuit  
which acquires information about said display unit;  
and

20 display-information memories which store the  
information about said display unit, and are connected  
to said data bus and said address bus so as to supply  
the information about said display unit to the  
exterior of said display device when so requested.

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9. The display device as claimed in claim 8,  
wherein said display-information acquisition circuit  
checks said display unit to acquire the information  
30 about the said display unit with regard to a defect of  
said display unit.

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10. The display device as claimed in claim  
8, wherein said display-information acquisition

1 circuit acquires the information about the said  
display unit with regard to coordinates of a position  
at which input is entered on said display unit.

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11. The display device as claimed in claim  
2, wherein said display unit includes:

10 a plurality of polysilicon thin-film  
transistors; and

a plurality of pixel electrodes  
corresponding to the respective polysilicon thin-film  
transistors, wherein display data is supplied to the  
15 pixel electrodes via the polysilicon thin-film  
transistors selected by said gate driver and said data  
driver.

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